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POTTER LINE PRINTER MODEL LP 3000 AND INTERFACE TO THE DDP-116 SITE COMPUTERS

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Introduction

Two medium speed line printers for use off-line with the DDP-116 site computers for computer diagnostics and program debugging have been procured. The specifications are as follows:

Speed	Up to 135 lines per minute
Print Positions	132 print positions per line
Ribbon	Conventional 3/4-inch horizontal tabulation
Forms	Accepts standard pin-feed forms up to 15 inches wide with 1/2 inch hole centers, up to 3 parts thickness.
Print Area	13 1/4 inches wide.
Character Spacing	10 characters per inch.
Line Spacing	6 lines per inch.
Line Print Cycle Time	385 milliseconds
Character Type	Open gothic, 35 dot matrix arranged as 5 columns and 7 rows with 2 blank columns and 5 blank rows between characters.
Paper Shift Speed	1/4 inch per second during printing; 2 1/2 inches per second between lines; 10 inches per second during line feed (FDXXLN).

The instruction set is arranged as follows:

OCP'0006	End of line. Force prints a line when less than 132 characters have been loaded into the printer line memory by filling blanks in all unused character positions. This instruction should not be issued when 132 characters are loaded into the line memory since the printer automatically prints after 132 characters are loaded.
OCP'0106	Top of form. Advances paper to next top of form. This instruction should only be used following an interrupt or an SKS 006.

SKS'0006

Skip if ready to receive a character.

SKS'0106

Skip if printer is on line. The printer will initially be off line when power is applied. A front panel pushbutton sets the printer on line. The printer will go off line if the paper, ribbon, or carriage is out. If the printer goes off line after a portion of a line has been loaded, then the line may be completed and printed but no more interrupts will occur until the printer is manually returned to on line status.

OTA'0120

Set mask. If bit 9 of the A-register is set the line printer interrupt mode will be masked on allowing an interrupt to occur at the end of each print cycle or when the printer is set on line. When an interrupt occurs, the computer generates a jump and store location instruction indirectly through location 00040.

OTA'0006

Output to line printer. If the printer is ready to receive a character, the next instruction will be skipped, and the contents of the output bus will be sent to the line printer buffer memory. Bits 9 through 16 form the character to be transferred according to the following table:

Octal Character

ode	Potter LP 3000		Teletype ASR 33
_	0, 2, 3	1-	0 1 2 3
0 0	@	FD64LN	Null @
0 1	A	${ t FD63LN}$	SOM A
0 2	В	FD63LN	EOA B
0 3	C	FD61LN	EOM C
0 4	D	${ t FD}60{ t LN}$	EOT D
0 5	E	${ t FD59LN}$	WRU E
0 6	F	${ t FD58LN}$	RU F
0 7	G	FD57LN	Bell G
1 0	H	${ t FD56LN}$	FE _O H
1 1	I	FD55LN	H. Tab I
1 2	J	${ t FD54LN}$	Line Feed J
13	K	${ m FD}53{ m LN}$	V. Tab K
14	L	FD52LN	Form L
1 5	M	FD51LN	Carriage M Return
16	N	${f FD}$ 50LN	S0 N

-	4	0, 2, 3	1-	- 0-	← 1−	2 ——	3
♥							
	17	0	FD49LN			S1	0
	2 0	Р	${ m FD48LN}$			DC_0	P
	2 1	Q	FD47LN		7	Start Code	Q
	0.0		ETACT N			(X-On)	
	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	R S	FD46LN			Tape Aux On	R S
	23	٥	PD45LN			Stop Code (X-Off)	b
	2 4	T	FD44LN			Tape Aux Off	T
	2 5	U	FD43LN		2	Error	U
	2 6	V	FD42LN			Sync	v
	27	W	${ m FD41LN}$			LEM	w
	3 0	X	${ m FD40LN}$			S_0	X
	3 1	Y	FD39LN	1		S_1	Y
	3 2	\mathbf{Z}	FD38LN			S_2	Z
	3 3	[FD37LN	\		S_3	[
	3 4		FD36LN			S ₄	
	3 5]	FD35LN			S ₅	
	3 6		FD34LN	}		S	•
	3 7	_ Underline	FD33LN	ļ		S ₆ S ₇	4-
	4 0	Blank	FD32LN			[°] Blank	
	4 1	!	FD31LN			!	
	4 2	" Quote	FD30LN			" Quote	1
	4 3	#	${ m FD}29{ m LN}$			#	
	4 4	\$	FD28LN			\$	
	4 5	%	FD27LN			%	
	4 6	& Ampersand	FD26LN			& Ampersand	
	47	' Apostrophe	FD25LN			' Apostrophe	
	5 0	(FD24LN			(
	5 1)	FD23LN	})	
	5 2	*	FD22LN			*	
	5 3	+	FD21LN	Ì		+	
	5 4	, Comma	FD20LN			, Comma	
	5 5	- Dash	FD19LN			- Dash	
	5 6	, Period	FD13LN			. Period	
	5 7	/	FD17LN	}		/	
	6 0	0	FD16LN			0	
	6 1	1	FD15LN			1	
	6 2	2	FD14LN			2	
	6 3	3	FD13LN			3	
	6 4	4	FD12LN			4	
	6 5	5	FD11LN			5	
	6 6	6	FD10LN			6	
	6 7 7	7	FD9LN			7	
	7 0	8	FD8LN			8	
	7 1	9	FD7LN			9	
	7 2	: Colon	FD6LN	1		: Colon	
	7 3	; Semicolon	FD5LN		l	; Semicolon	1
	74	<	FD4LN	1		<	Ack
	75	=	FD3LN			=	Alt Mode ESC
	$\begin{array}{c} 7 \ 6 \\ 7 \ 7 \end{array}$	> ?	FD2LN FD1LN			> ?	Rub Out

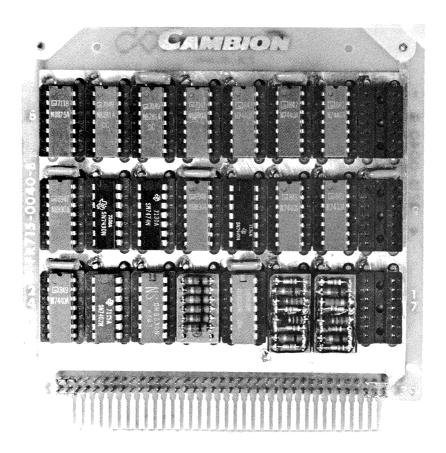
In the table, a comparison of the teletype and line printer 8-bit character codes is given. Along the left most column is presented a 2-digit code 00 to 77, representing the 6 least significant bits of the A-register. In the teletype two additional bits 9 and 10 are used to define which column (2 or 3) is printed. Columns 0 and 1 have no meaning in the teletype output. The character codes have been set up for the Potter line printer so that any alphnumeric character that has been set up to be printed on the teletype may also be sent to the line printer for printing with only two character changes. They are the \(\bigcirc\) which has been changed to \(\bigcirc\) and the \(\bigcirc\) which has been changed to the ___ (underline). Control characters of the teletype are not used. If, however, one of these characters is sent to the line printer, a printed output of the character with the same 2-digit octal code as the control character will be printed.

In the Potter line printer with the options procured with it, only one line feed may be issued between print cycles. To skip more than one line it is necessary to force print blank lines at print cycle speed which is slow compared to the paper slew speed, 1/3 I.P.S. vs. 10 I.P.C. Accordingly, a simple logic scheme has been planned which employs a line counter which may be preset with a 6-bit 2's complement octal code defining the number of lines to be skipped. According to column 1 of the table, from 1 to 64 lines may be skipped at slew speeds by loading the A-register with the proper 1XX code and executing the standard OTA'0006 character load instruction. The paper slew sequence (FDXXLN) should only be issued immediately following an interrupt.

A special demand case of the above line skip feature occurs when it is desired to issue both top of form and FDXXLN instructions in succession as would be the case if it is desired to skip down a few lines from the top of the form such as in a title page. The proper sequence is to issue a top of form instruction followed by an end of line instruction, then waiting for the interrupt that occurs after the print cycle to issue the FDXXLN instruction with XX being equal to the number of lines to skip minus one for the forced printed blank line. This sequence is required in this special case since both FDXXLN and top of form instructions have the requirement that they only be issued immediately following an interrupt through location 00040.

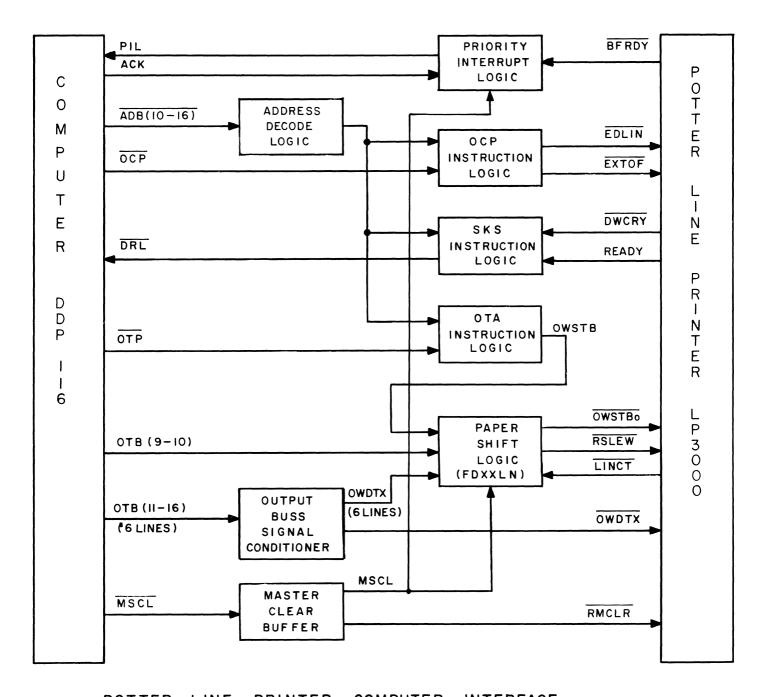


Line Printer and DDP-116 Computer



Interface Electronics Card

Description: All active control signals are shown in the following block diagram:



POTTER LINE PRINTER COMPUTER INTERFACE
BLOCK DIAGRAM

FIG.A.

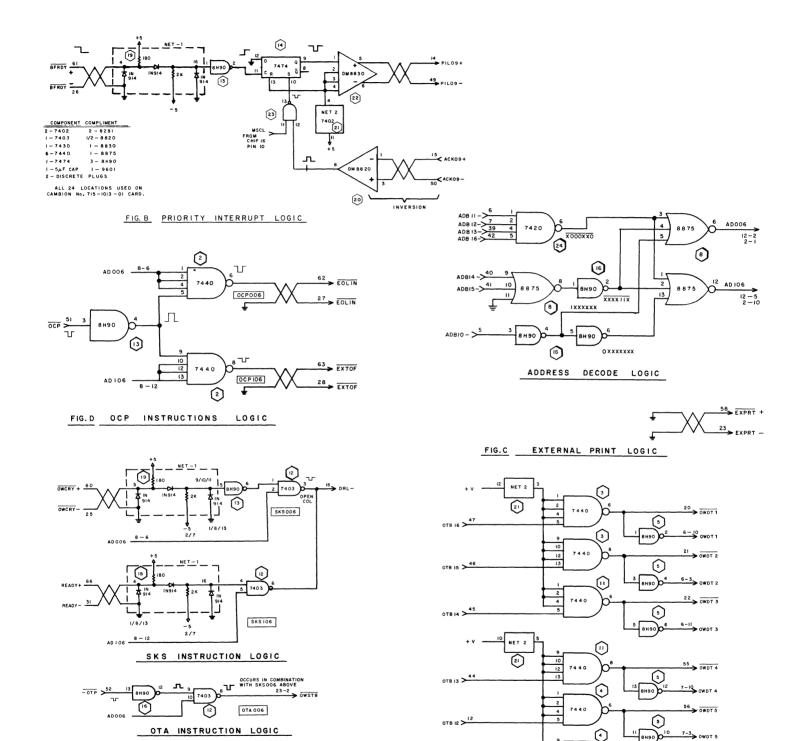
The operation is sequential as follows: (See Block Diagram.)

When the printer buffer register is empty, BFRDY goes low and a PIL is issued to the DDP-116 computer from the priority interrupt logic. As soon as the computer "sees" the signal the program is interrupted and control jumps indirect through memory cell 40)₈ containing the pointer to the line printer interrupt service subroutine (ISS). At this time the computer automatically issues the ACK (acknowledge) pulse back to the PIL logic thus clearing the PIL. The above sequence occurs when the printer has finished printing a line. The (ISS) now refills the character buffer by setting up the proper address as controlled by the 7 LSB's of the instruction register through ADB 10-16 which accordingly enables the OCP logic to advance paper, SKS printer status test logic, or the OTA character output to printer buffer or control to paper shift logic (FDXXLN).

The computer output buss is buffered and sent to the paper shift logic or line printer data receiver lines. DDP-116 master clear initializes the printer.

Circuit Description

Standard TTL logic is employed in the circuits which are self-explanatory to those skilled in computers. Signals are transmitted wherever possible by twisted pair cable thus minimizing ground system problems. The circuit schematics are presented on the next pages.

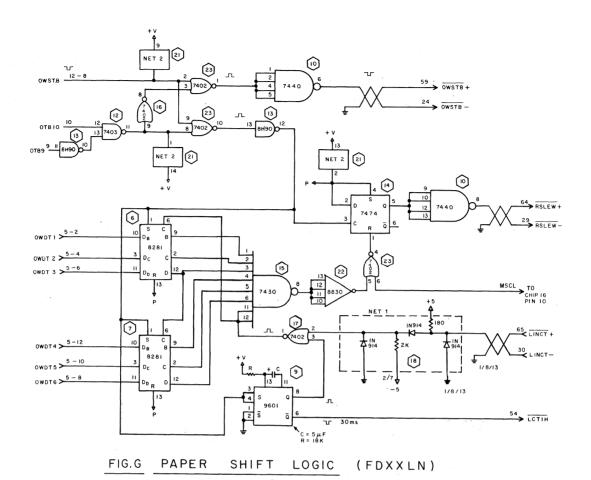


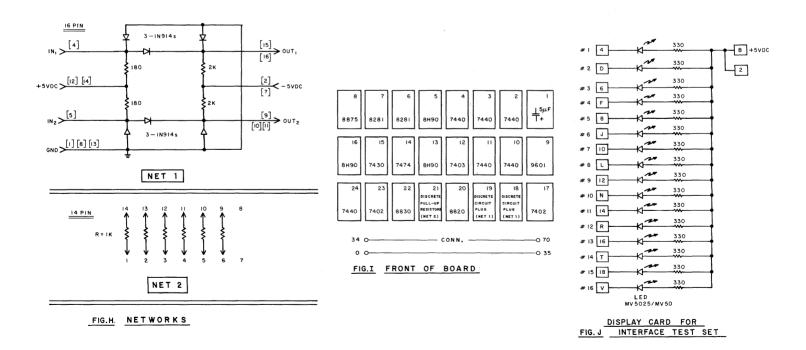
MASTER CLEAR BUFFER

FIG.E

FIG.F OUTPUT BUSS SIGNAL CONDITIONER

(3)





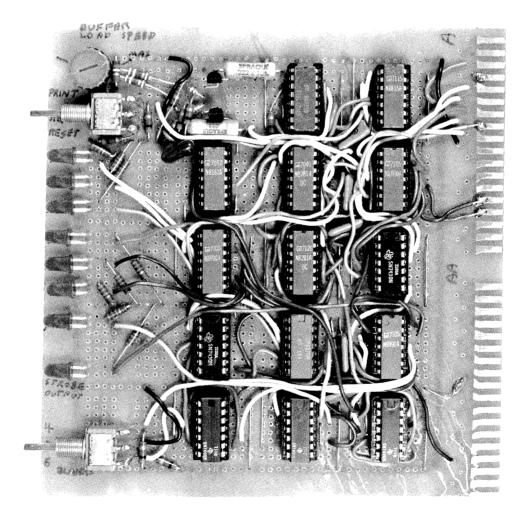
Printer Diagnostic Test Card

An excersizer card is available to test the printer. It may be installed in the line printer card file by removing the back cover from the line printer and inserting the card in place of the computer interface cable. With the printer "on line" three patterns may be printed which sequence through every possible character printable as shown.

ABCDE	ABCDE	EFGHI
BCDEF	ABCDE	DEFGH
CDEFG	ABCDE	CDEFG
DEFGH	ABCDE	BCDEF

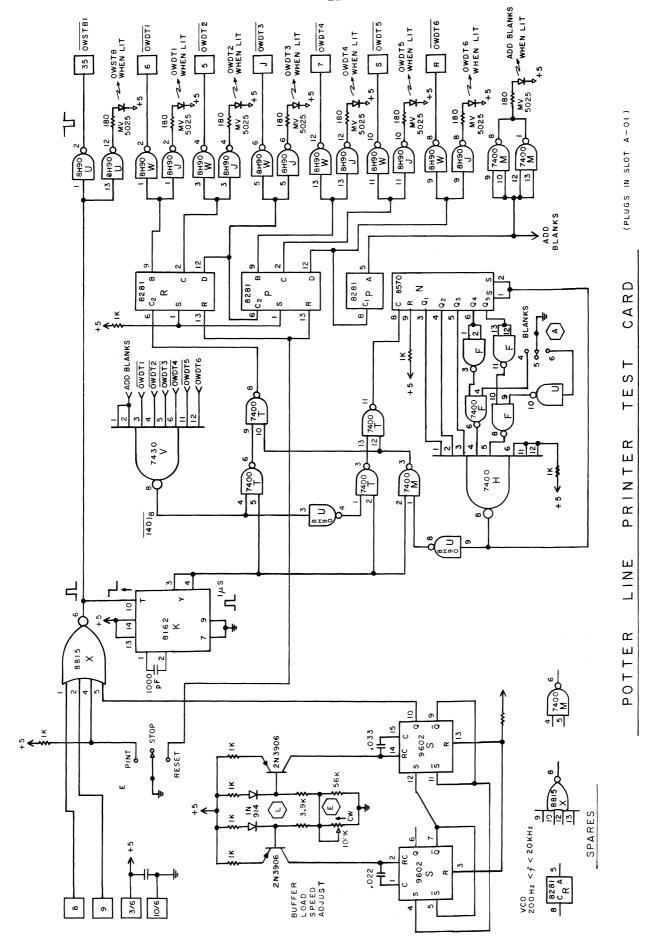
4 - Rotate left. 5 - Stationary. 6 - Rotate right.

Two toggle switches and a pot are on the card. A toggle selects either pattern 4, 5, or 6 by logically inserting 4, 5, or 6 extra blanks when character 140)₈ is detected. The other switch resets, stops, or prints. The pot controls the character buffer refill speed thus controlling the printing speed.



Printer Exerciser Test Card.

The test card schematic, printer wiring tables, and interface card test procedure follow.



Line Printer Interface Card Pin Out

	Name	From	То		Name	From	То
1	Ground			36	+5		
2	Ground			37	+5		
3				38			
4				39	ADB 13		
5	ADB 10			40	ADB 14		
6	ADB 11			41	ADB 15		
7	ADB 12			42	ADB 16		
8				43_			
9	OTB 09			44	OTB 13		
10	OTB 10			45	OTB 14		
11	OTB 11			46	OTB 15		
12	OTB 12			47	OTB 16		
13				48			
14	PIL09+			49	PIL 09-		
15	ACK09+			50	ACK09-		
16				51	OCP-		
17				52	OTP-		
18	DRL-			53	MSCL-		
_19				54			
20	OWDT1			55	OWDT4		
21	OWDT2			56	OWDT5		
22	OWDT3			57	OWDT6		
23_	* EXPRT-			58	EXPRT+		
24	* OWSTB-			59	OWSTB+		
25	* OWCRY-			60	OWCRY+_		
26	* BFRDY-			61	BFRDY+		
27	* EOLIN-			62	EOLIN+		
28	* EXTOF-			63	EXTOF+		
29	* RSLFW-			64	RSLFW+		
30	* LINCT-			65	LINCT+		
31	* READY-			66	READY+		
32	* RMCLR-			67	RMCLR+		
33	-5 V DC			68	GND		
34	Ground			69	+5		
35	Ground			70	+5		

FRONT BACK (Wire Wrap Pin Side) (Socket Side)

^{*} Single ended signal
- = Return)
+ = Signal) on twisted pair

POTTER LINE PRINTER INTERFACE CABLE

Wire	Elco	Potter Card 01	Wire	Elco	Potter
<u>Color</u>	Connector		<u>Color</u>	Connector	Card 01
Blk	А	6	Red	Y	Gnd
Brn	В		Grn	Z	NN
Blk	C	5	Red	AA	Gnd
Red	D	S	Blu	BB	31
Blk	E	J	Red	CC	Gnd
Orn	F	R	Wht	DD	28
Blk	H	Gnd	Grn	EE	
Yel	J	35	Brn	FF	
Blk	K	Gnd	Grn	ЛЛ	
Grn	L	8	Orn	НН	
Blk	M	Gnd	Grn	KK	
Blu	N	9	Yel	LL	
Blk	P	Gnd	Grn	MM	
Wht	R	26	Blu	NN	
Red	S	Gnd	Grn	PP	
Brn	T	D	Wht	RR	
Red	U	Gnd	Blu	SS	V
Orn	V	27	Wht	TT	Gnd
Red Yel	W X	Gnd 12			

NOTE: 1. Except for pairs AB, CD, EF, the return conductor (gnd designation of each pair) should be connected to nearest ground hole in Potter Card 01.

- 14 -

PRINTER INTERFACE CARD TO LINE PRINTER LINK TO CONNECTOR

Cable Elco	Card Edge Connector		Cable <u>Elco</u>	Card Edge Connector	
A	20	OWDT1	AA	32	RMCLR-
В	55	OWDT4	BB	67	RMCLR+
С	21	OWDT2	CC	23	EXPRT-
D	56	OWDT5	DD	58	EXPRT+
E	22	OWDT3	EE)	
F	57	OWDT6	\mathbf{FF}) SPARE	
Н	24	OWSTB-	нн)	
J	59	OWSTB+	IJ) SPARE	
K	25	OWCRY-	KK)	
L	60	OWCRY+	$\mathbf{L}\mathbf{L}$) SPARE	
M	26	BFRDY-	MM)	
N	61	BFRDY+	NN) SPARE	
P	27	EOLIN-	PP)	
R	62	EOLIN+	RR) SPARE	
S	28	EXTOF-	SS	33	-5 V DC
Т	63	EXTOF+	TT	68	GND
U	29	RSLEW-			
v	64	RSLEW+			
W	30	LINCT-			
x	65	LINCT+			
Y	31	READY-			
${f z}$	66	READY+			

19 PAIR CABLE EXAMPLE OF PAIRS: A-B, C-D, E-F, etc.

POTTER PRINTER INTERFACE LOGIC CARD TEST PROCEDURE

1. Priority Interrupt Logic Test

- A. Reset switch 10.
- B. Set switch 5.
- C. Reset switch 6.
- D. Toggle switch 10.
- E. Observe state of lamps to be 1 on, 2 off.
- F. Toggle both switches 5 and 6 together.
- G. Observe that lamps 1 and 2 change state.
- H. Repeat steps D through G.

2. Address Decode Logic Test 006

- I. Set switches 11, 14, 15, 16, and 17.
- J. Reset switches 12 and 13.
- K. Proceed to OCP 006 test and if passed then this test is also passed.

3. Address Decode Logic Test 106

- L. Set switches 11, 14, 15, and 16.
- M. Reset switches 12, 13, and 17.
- N. Proceed to OCP 106 test and if passed then this test is also passed.

4. <u>OCP 006 Test</u>

- O. Set switch 7.
- P. Do steps I and J above.
- Q. Observe lamp 11 is off.
- R. Observe that when switch 7 is reset, lamp 11 comes on.
- S. With switch 7 reset, toggle switches 11 thru 17 one at a time and observe that lamp 11 goes off each time a switch is toggled. (This step fully static tests address logic 006 and if passed, then logic 006 is good under static conditions.)

5. <u>OCP 106 Test</u>

- T. Do step O above.
- U. Do steps L and M above.
- V. Observe lamp 12 is off.
- W. Observe that when switch 7 is reset, lamp 12 comes on.
- X. Do step S above and if passed then address 106 is good under static conditions.

POTTER PRINTER INTERFACE LOGIC CARD TEST PROCEDURE (continued):

6. SKS 006 Test

- Y. Set switch 9.
- Z. Do steps I and J above.
- AA. Observe lamp 3 is off.
- AB. Observe that when switch 9 is reset, lamp 3 comes on.
- AC. With switch 9 reset, toggle switches 11 thru 16 one at a time and observe that lamp 3 goes off each time a switch is toggled.

7. SKS 106 Test

- AD. Reset switch 20 and do steps L, M, and AA above.
- AE. Observe that when switch 20 is set lamp 3 comes on.
- AF. With switch 20 set, toggle switches 11 thru 16 one at a time and observe that lamp 3 goes off each time a switch is toggled.

8. OTA 006 Test

- AG. Set switch 8.
- AH. Do steps I and J above.
- AI. Reset switch 4.
- AJ. Reset switch 3.
- AK. Observe that lamp 10 is off.
- AL. Observe that when switch 8 is reset, lamp 10 goes on.
- AM. With switch 8 reset, toggle switches 11 thru 17, and 3 and observe that lamp 10 goes off each time a switch is toggled.

9. Output Buss Signal Conditioner Test

- AN. Reset switches 1 and 2.
- AO. Observe that lamps 4, 5, 6, 7, 8, and 9 are not lit.
- AP. Toggle switch 1 and observe that each time it is set, lamps 4, 6, and 8 come on.
- AQ. Toggle switch 2 and observe that each time it is set, lamps 5, 7, and 9 come on.

10. Master Clear Test

- AR. Set switch 18.
- AS. Observe that lamp 14 is off.
- AT. Toggle switch 18 and observe that lamp 14 comes on each time it is reset.

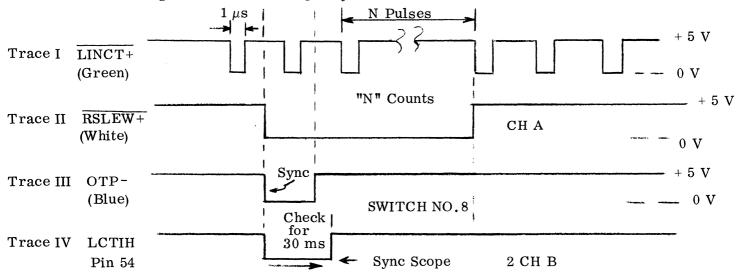
11. External Print Logic Test

AU. Observe that lamps 15 and 16 are lit.

POTTER PRINTER INTERFACE LOGIC CARD TEST PROCEDURE (continued):

12. Paper Shift Logic Test

AV. Inspect the following diagram where in trace I is a pulse (LINCT+) generator with a frequency of 1 kHz connected to the green binding post.



Trace II is the RSLEW+ signal as indicated by lamp 13 (on when signal is low) which is also present at the white binding post. Trace III is the OTP-pulse controlled by switch 8 (which is set when signal is high) present at the blue binding post.

- SW. Do steps I, J, and AI above.
- AX. Set switch 3.
- AY. Set up the test equipment according to the diagram of step AV. If two pulse generators are available, trace III may be obtained from the second pulse generator when switch 8 is adjusted to the center position and the pulse generator is set to 10 Hz frequency and $1 \mu sec$ pulse negative pulse width.
- AZ. Reset switches 1 and 2 and observe that the quantity "N" in the diagram is 64.
- BA. Set switch 1 and observe N = 43.
- BB. Set switch 2 and observe N = 1.
- BC. Reset switch 1 and observe N = 22.
- BD. Toggle switches 3, 4, 11, 12, 13, 14, 15, 16, and 17 and observe that trace II above remains high when any switch is toggled.

INTERFACE CARD TEST FIXTURE DATA

From Switches	Elco	То	Comments
1	DX	AP, X, BL	OTB 12, 14
2	E	V, C, AS	OTB 11, 13
3	z	A	OTB 10
4	AU	DT	ОТВ 9
5	CU	CP	ACK +
6	DN	DL	ACK -
7	EH	EE	OCP-
8	R	N	OTP-
9	AK	AT	OWCRY +
10	BD	СТ	BFRDY +
"1" Pole	CK	BU	BUSS
"0" Pole	DD	н	BUSS
11	DY	DA	ADB 16
12	F	CF	ADB 15
13	AA	вт	ADB 14
14	AV	BA	ADB 13
15	BM	CD	ADB 12
16	CA	BR	ADB 11
17	cv	AY	ADB 10
18	DP	AH	MSCL-
19	EJ	AJ	LINCT +
20	S	ВС	SPARE
"1" Pole	AL	BU	BUSS
"0" Pole	BE	Н	BUSS
From Jones Strip	Elco	То	Comments
A	BU		+ 5
В	CL		+12
С	DE		-18
D	DZ	M	
E	Н		GND
Binding Post	Elco	То	Comments
Red	AB	EE	OCP-
Blue	AW	N	OTP-
White	BN	P	RSLEW +
Yellow	СВ		
Green	cw	AJ	LINCT +

From Douglas Card	Elco	То	Comments
4	DR	BX	PIL +
D	EK	cs	PIL-
6	Т	L	DRL-
F	AM	AZ	OWDT1
8	BF	BS	OWDT2
J	BY	CE	OWDT3
10	СМ	СН	OWDT4
L	DF	DB	OWDT5
12	EA	DW	OWDT6
N	J	Y	OWSTB+
14	AC	DM	EOLIN +
R	AX	EF	EXTOF+
16	BP	P	RSLEW +
T	CC	CJ	RMCLR+
18	CX	CZ	
v	DS	D	

Cambion	Elco	Comments	Cambion	Elco	Comments
3	К		30	AF	
4	AD		39	BA	ADB 13
5	AY	ADB 10	40	BT	ADB 14
6	BR	ADB 11	41	CF	ADB 15
7	CD	ADB 12	42	DA	ADB 16
8	CY		43	DV	
9	DT	OTB 09	44	С	OTB 13
10	A	OTB 10	45	x	OTB 14
11	v	OTB 11	46	AS	OTB 15
12	AP	OTB 12	47	BL	OTB 16
13	BJ		48	BZ	
14	BX	PIL+	49	cs	PIL-
15	CP	ACK +	50	DL	ACK-
16	DJ		51	EE	OCP-
17	EC		52	N	OTP-
18	L	DRL-	53	AH	MSCL-
19	AE		54	ВВ	
20	AZ	OWDT1	55	СН	OWDT4
21	BS	OWDT3	56	DB	OWDT5
22	CE	OWDT5	57	DW	OWDT6
23	CZ	EXPRT-	58	D	EXPRT +
24	DU	OWSTB-	59	Y	OWSTB+
25	В	OWCRY-	60	AT	OWCRY+
26	w	BFRDY-	61	СТ	BFRDY+
27	AR	EOLIN-	62	DM	EOLIN +
28	BK	EXTOF-	63	EF	EXTOF+
29	BY	RSLEW-	64	P	RSLEW +
30	CR	LINCT-	65	AJ	LINCT +
31	DK	READY-	66	BC	READY +
32	ED	RMCLR-	67	CJ	RMCLR+
33	M	-5 V DC	68	DC	